

FIG. 1

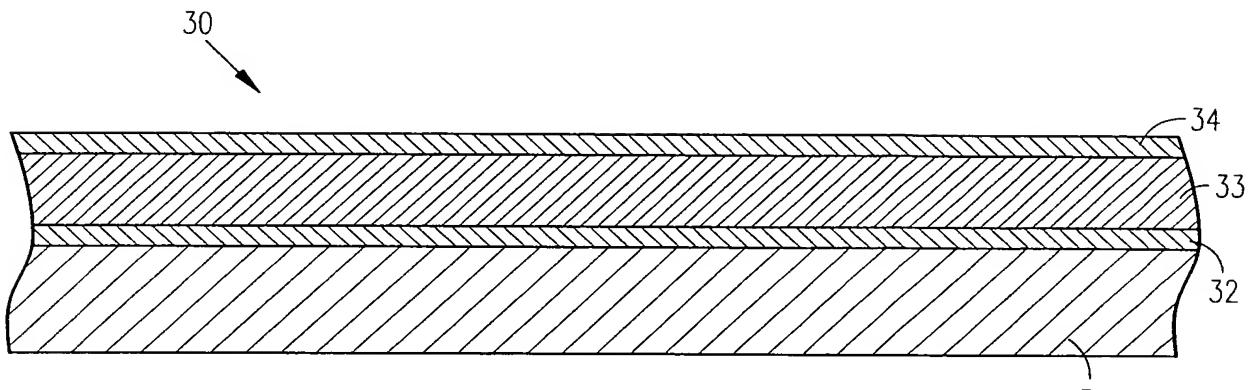


FIG. 2

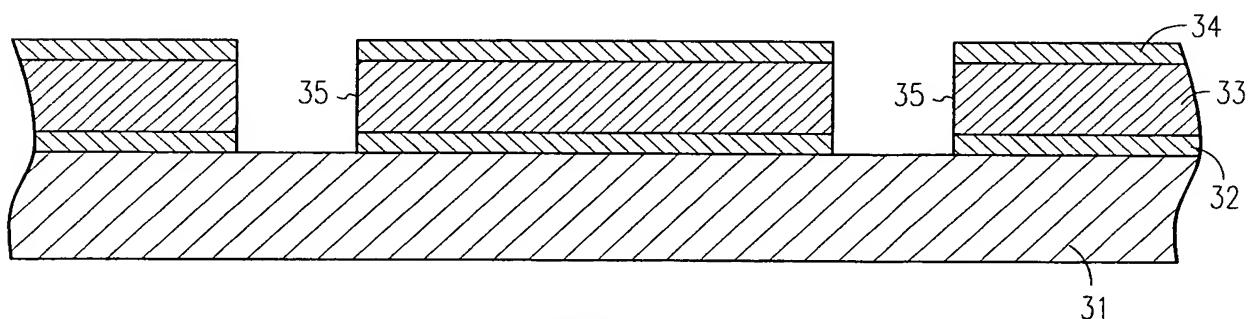


FIG. 3

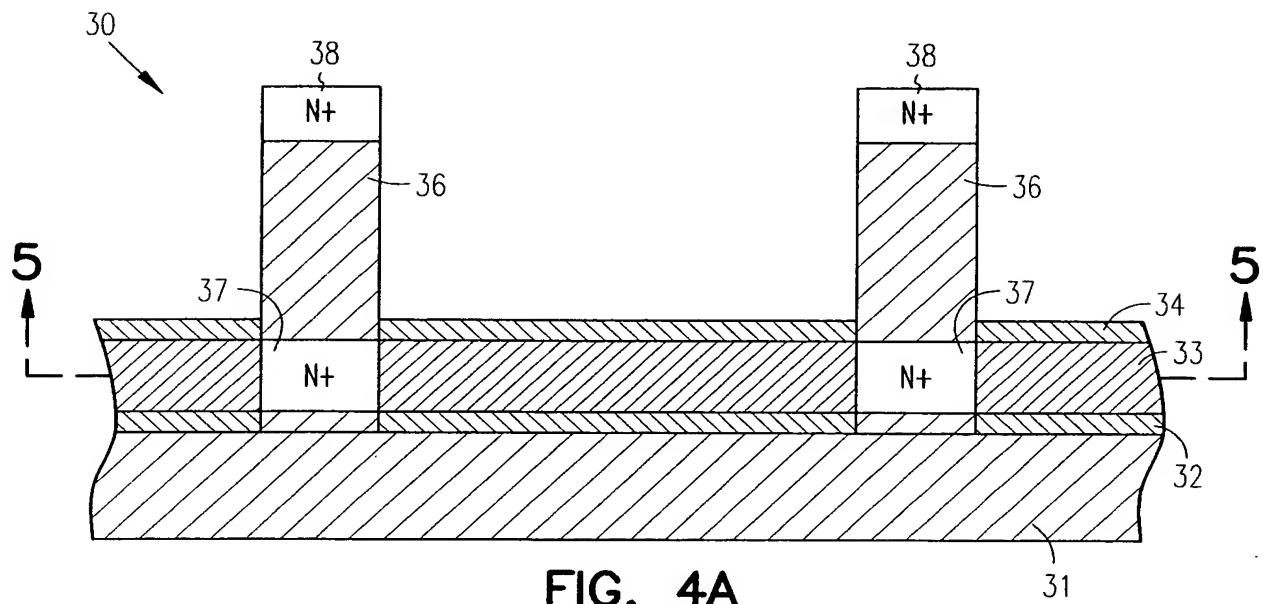


FIG. 4A

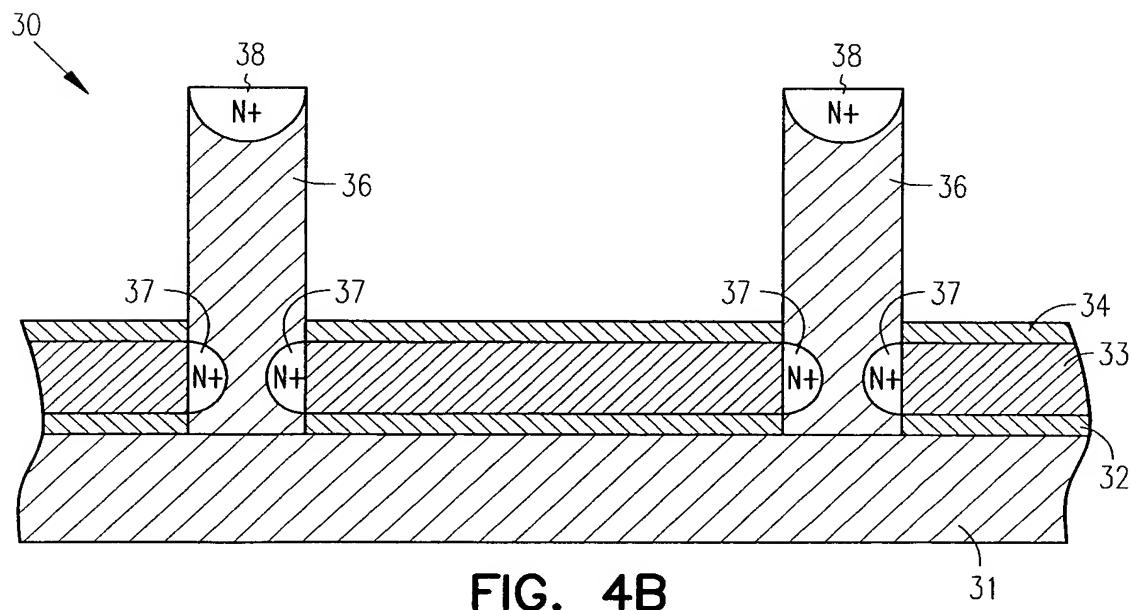


FIG. 4B

4/16

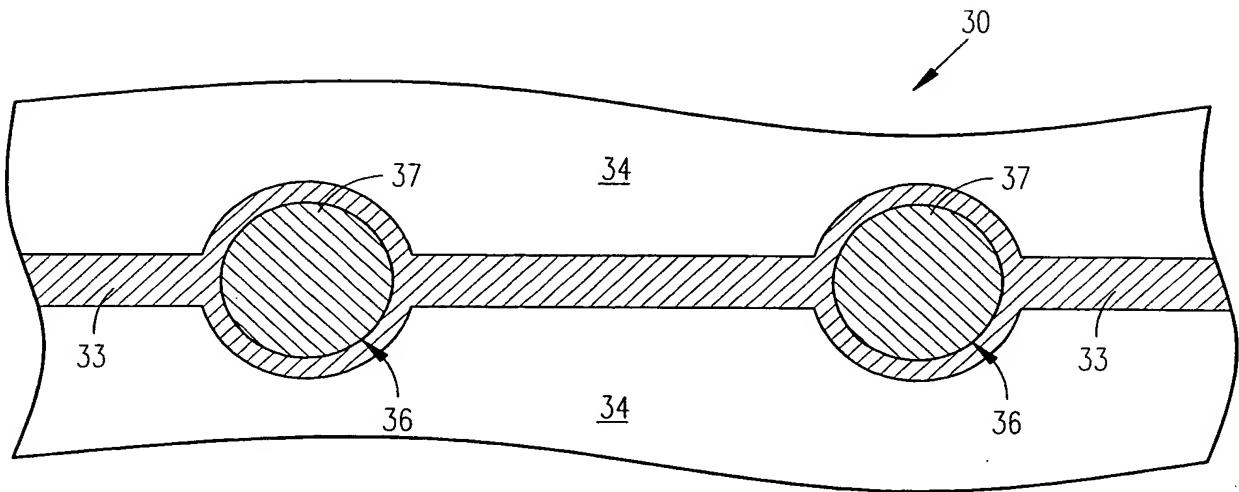


FIG. 5

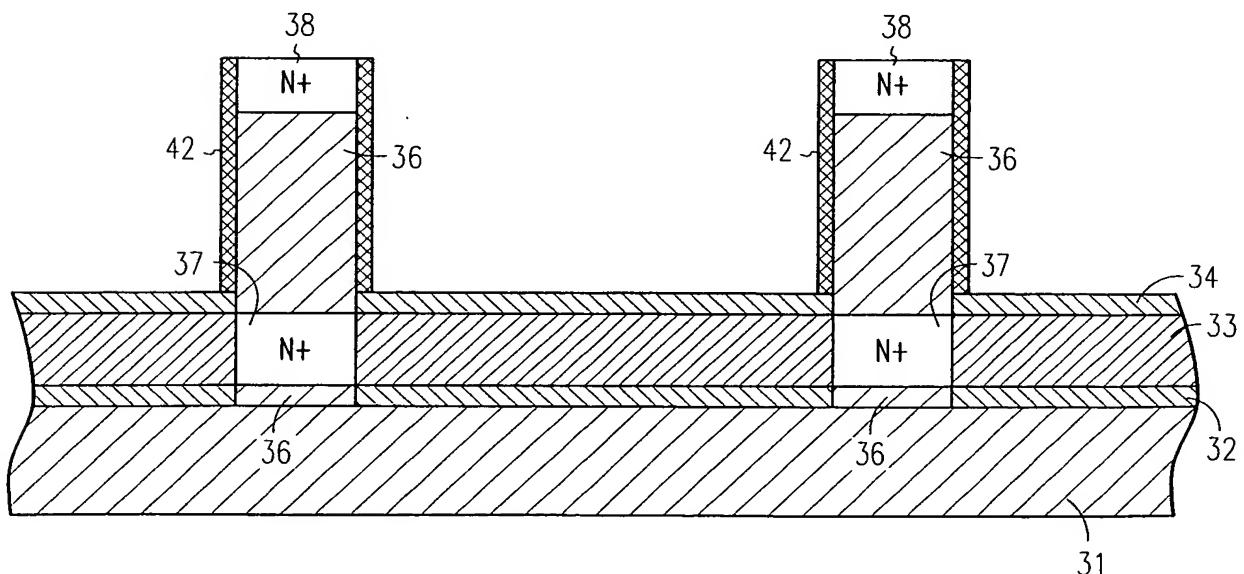


FIG. 6

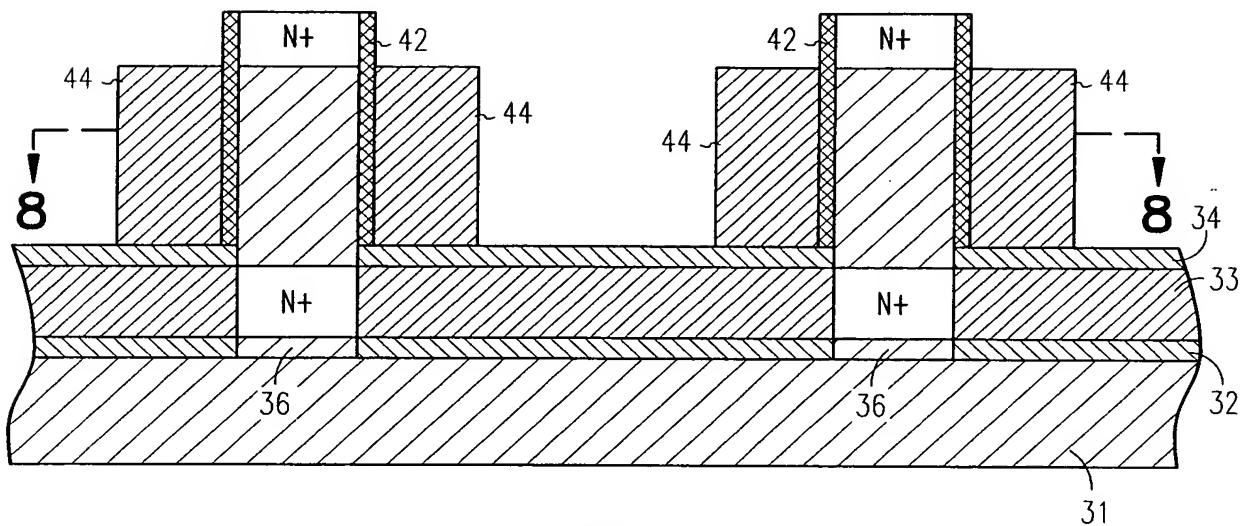


FIG. 7

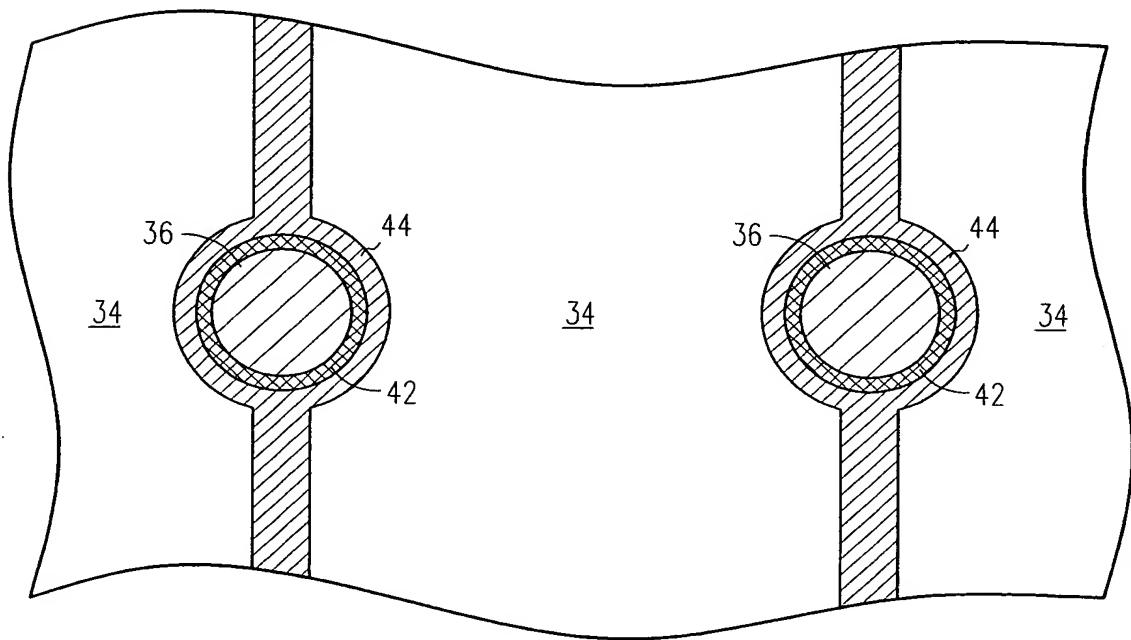


FIG. 8

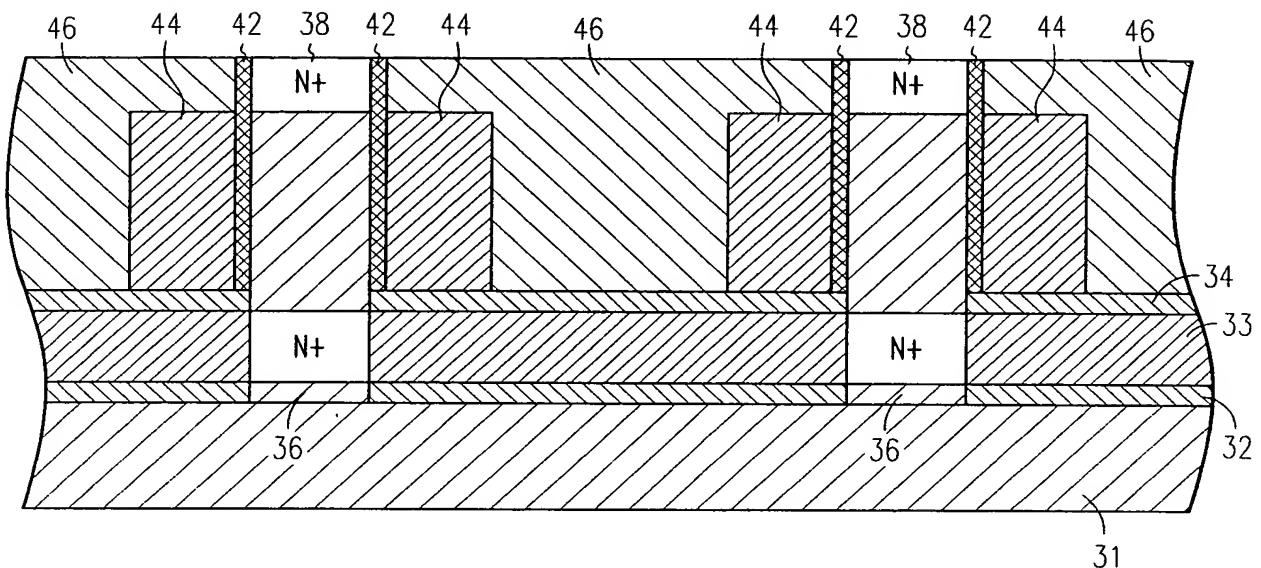


FIG. 9

7/16

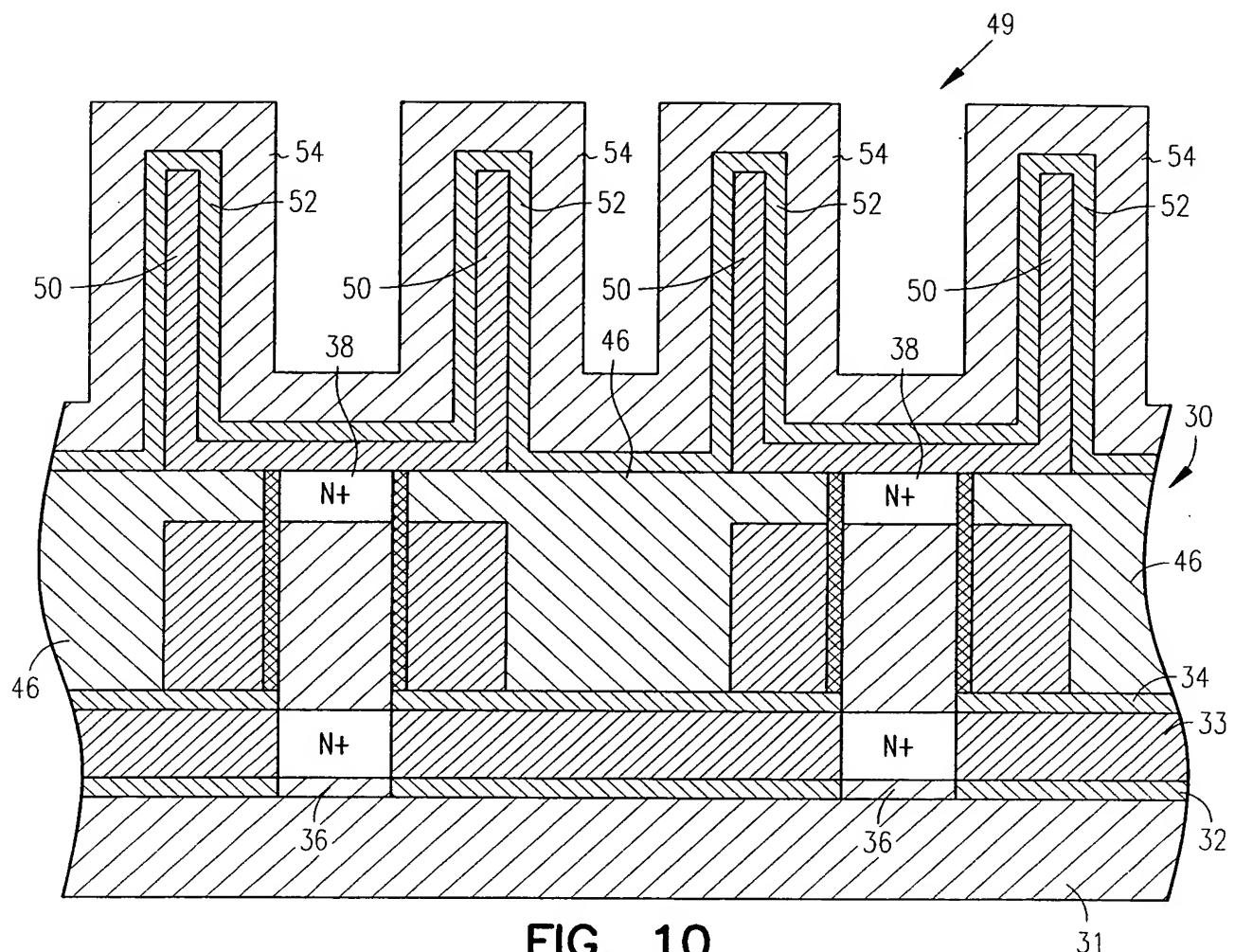


FIG. 10

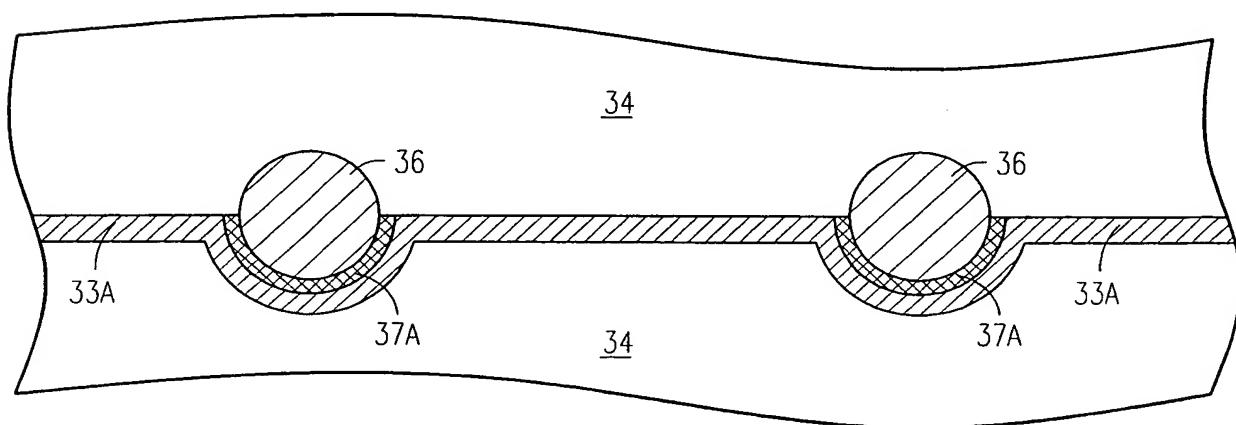


FIG. 11

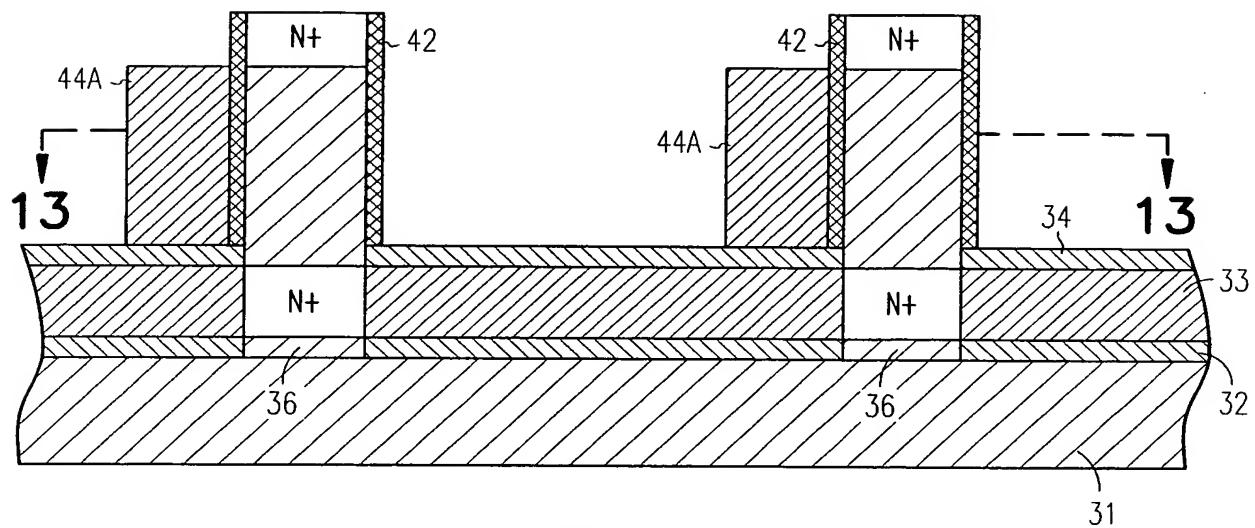


FIG. 12

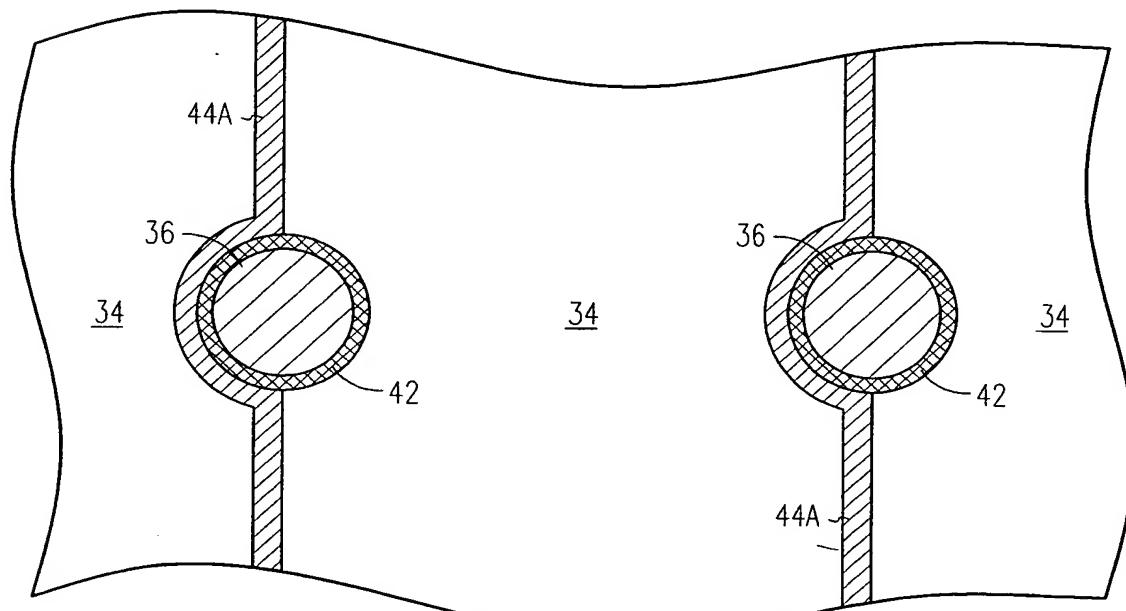


FIG. 13

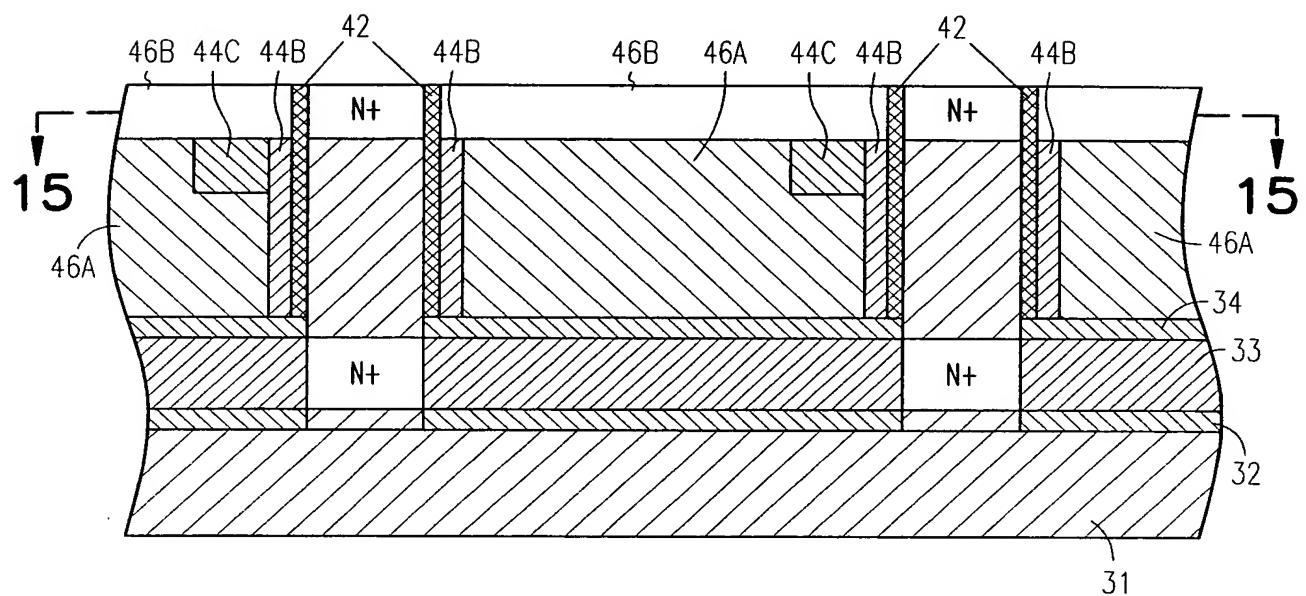


FIG. 14

10/16

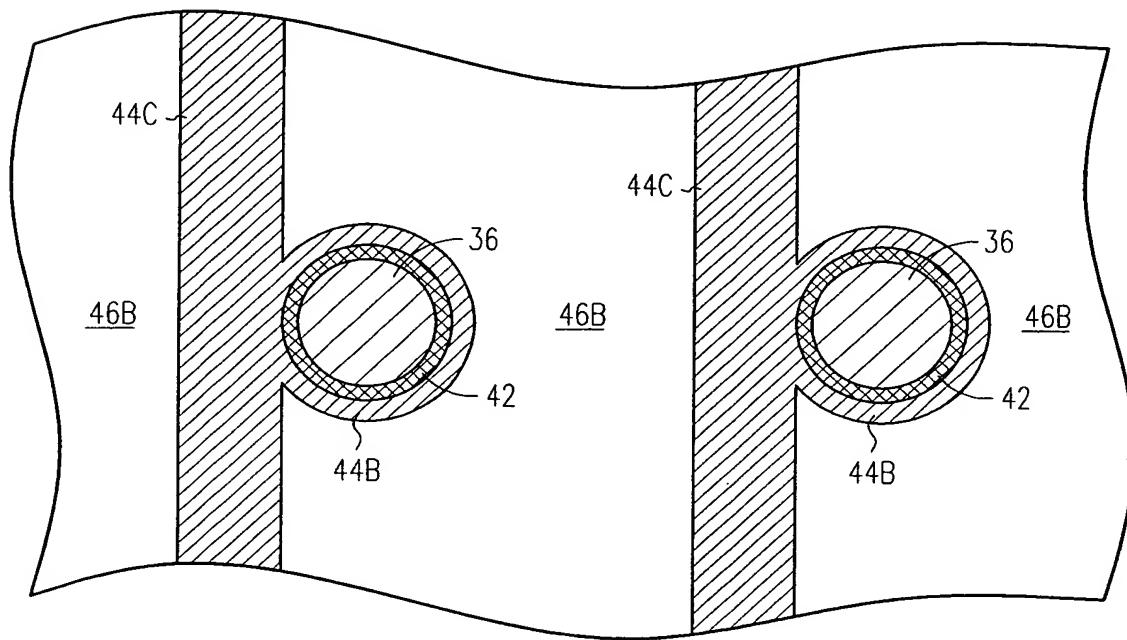


FIG. 15

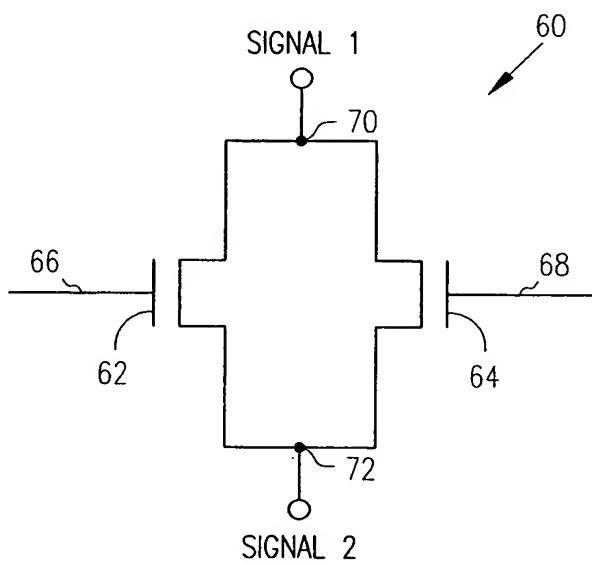


FIG. 16

11/16

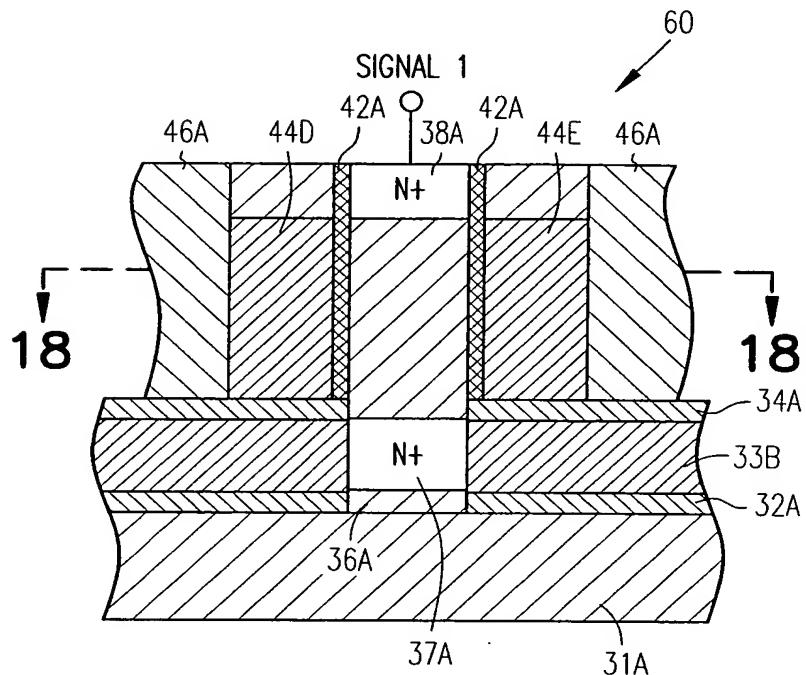


FIG. 17

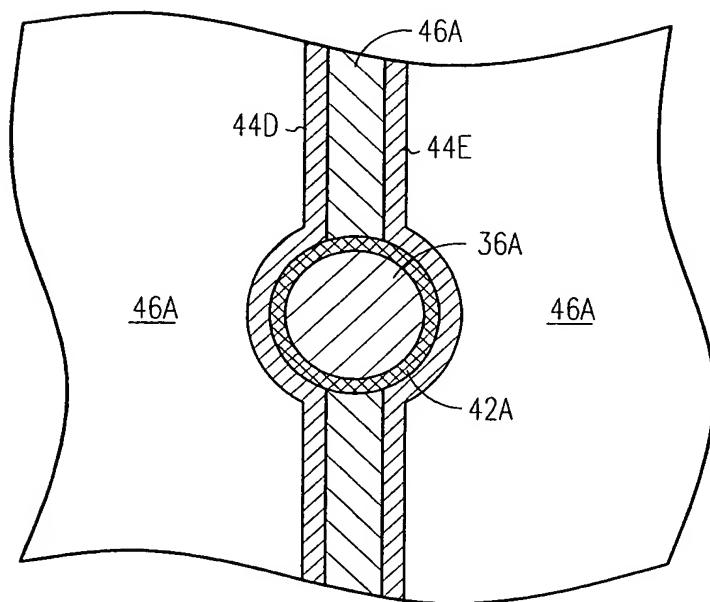


FIG. 18

TITLE: SELECTIVE EPITAXY VERTICAL INTEGRATED CIRCUIT COMPONENTS AND METHODS
INVENTORS NAME: Terrence C. Leslie
DOCKET NO.: 303.860US1

12/16

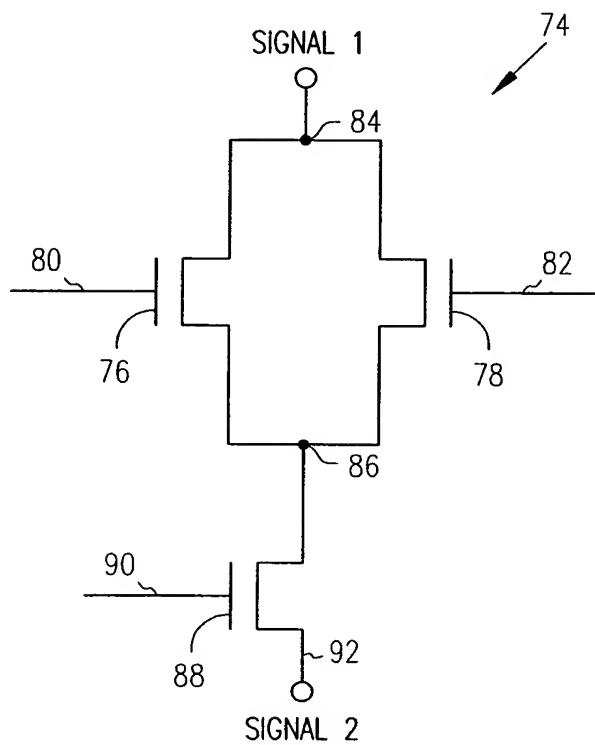


FIG. 19

13/16

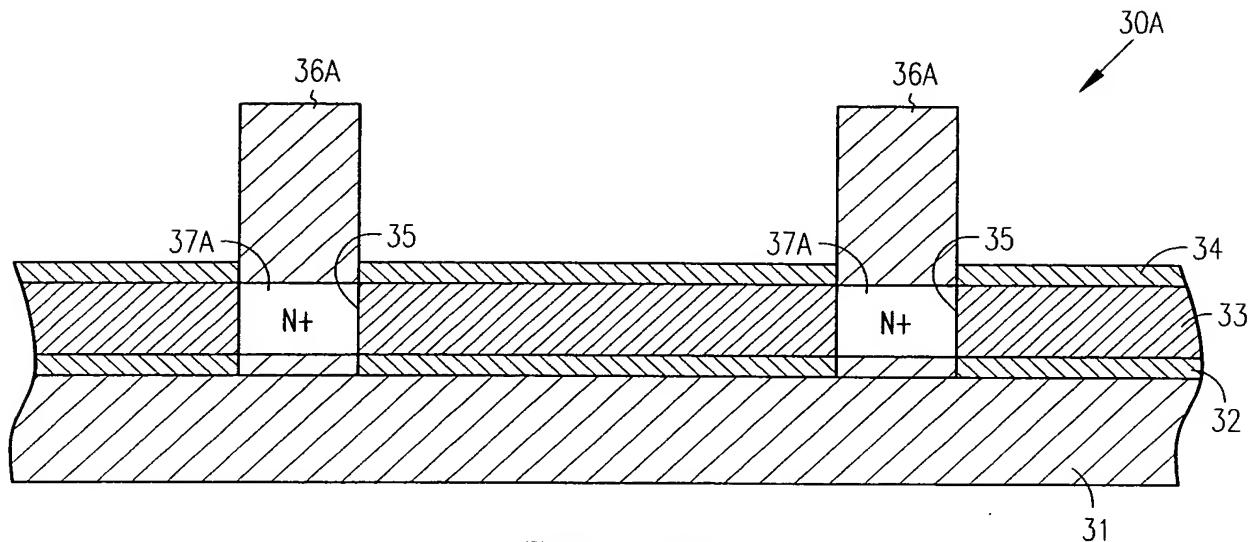


FIG. 20

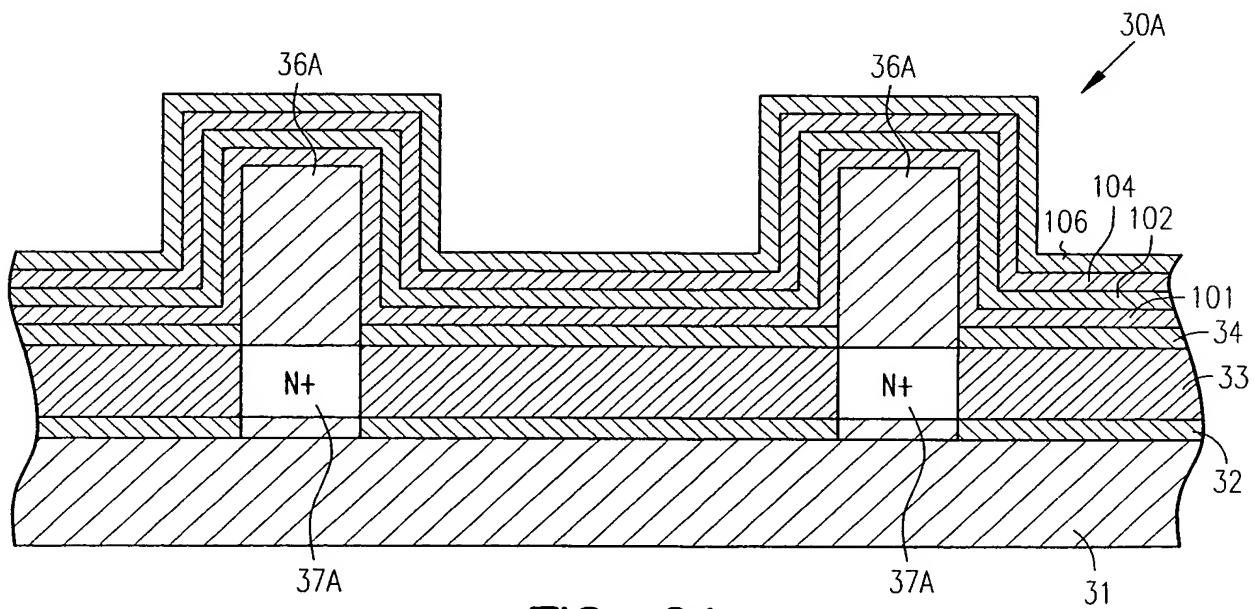


FIG. 21

14/16

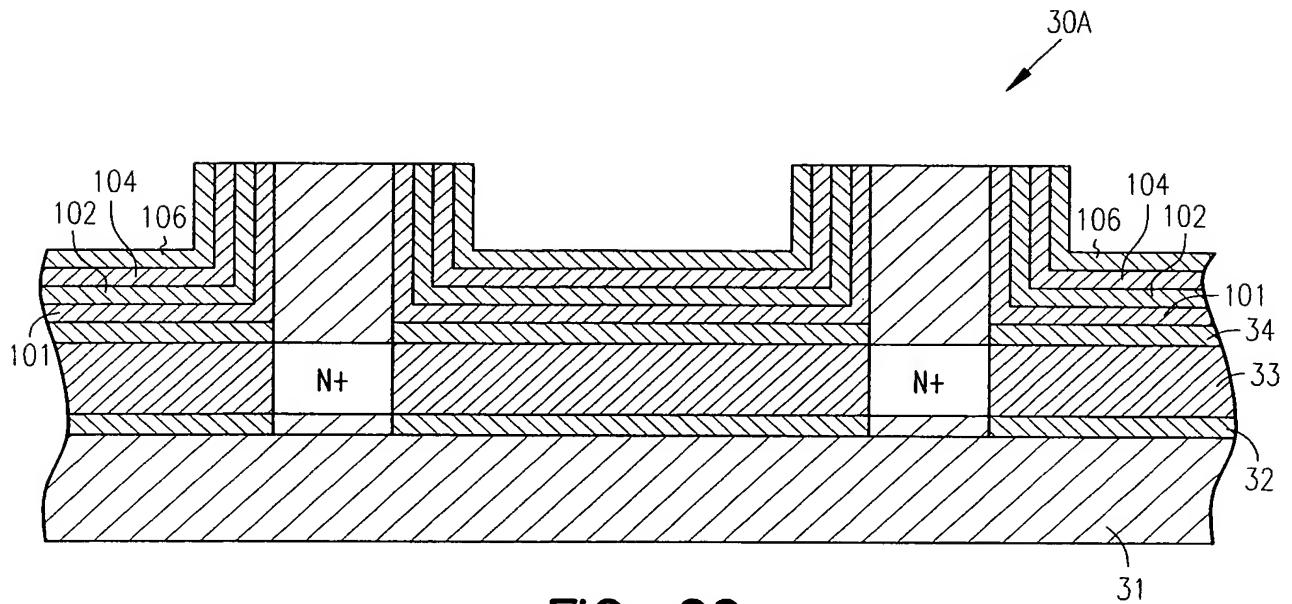


FIG. 22

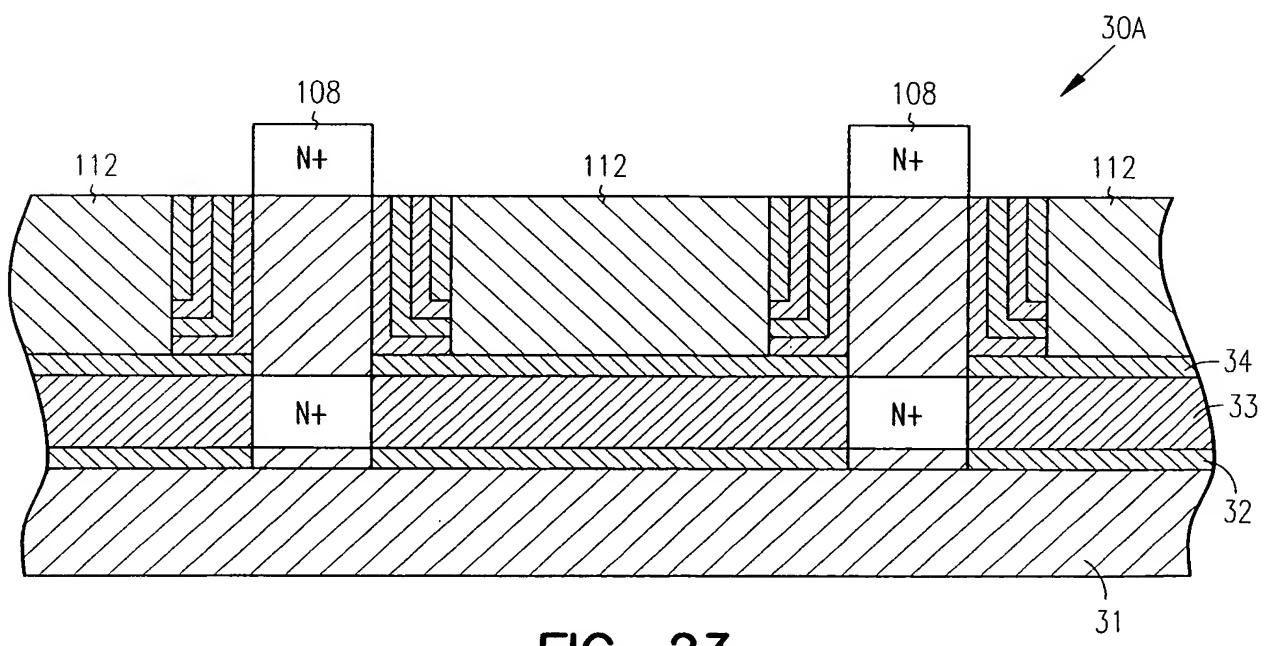


FIG. 23

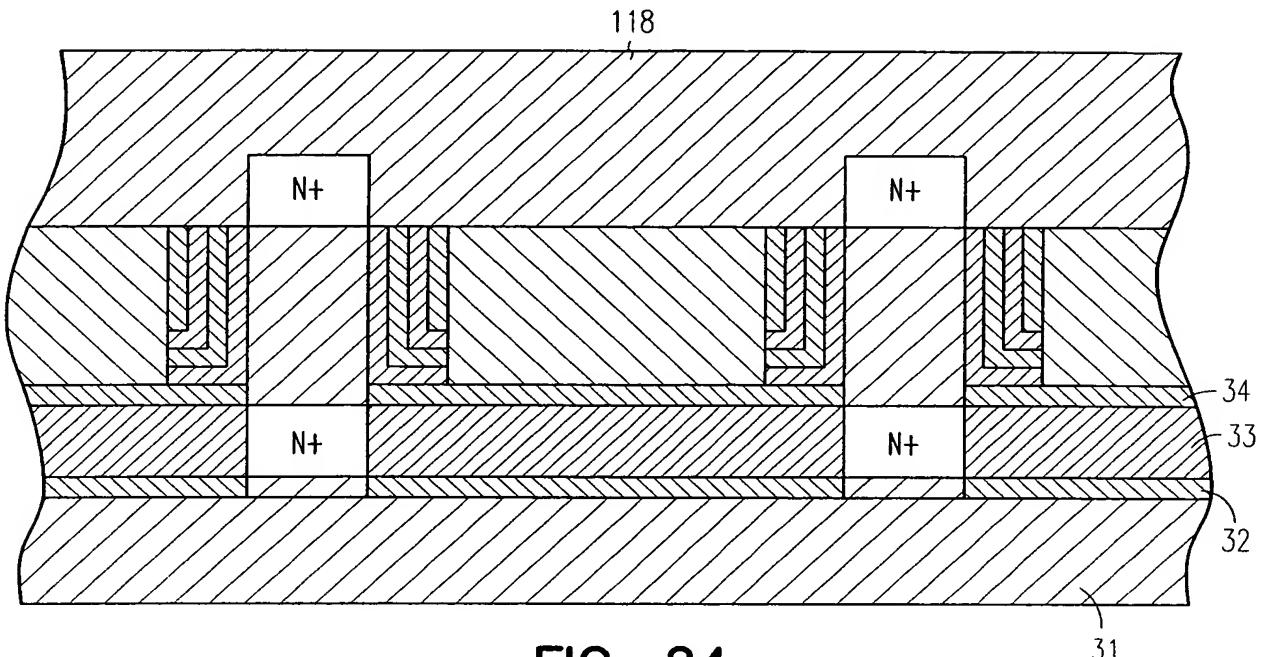


FIG. 24

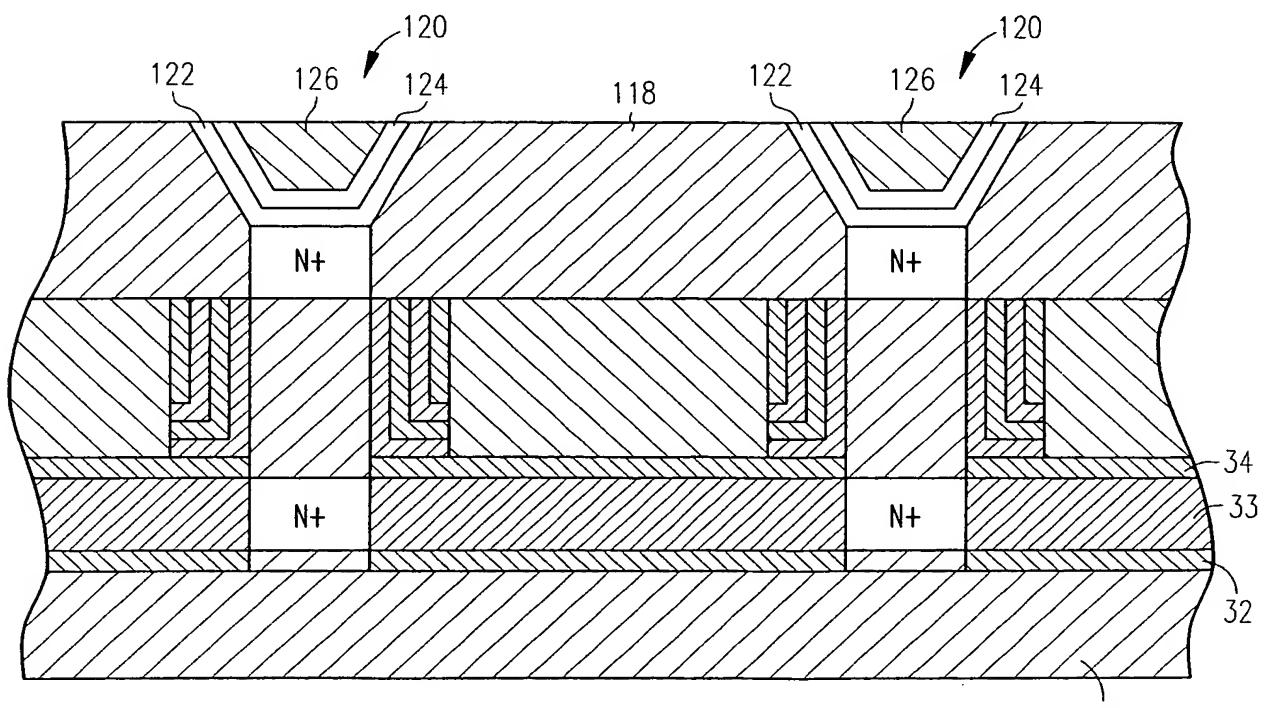


FIG. 25

TITLE: SELECTIVE EPITAXY VERTICAL INTEGRATED CIRCUIT COMPONENTS AND METHODS
INVENTORS NAME: Terrence C. Leslie
DOCKET NO.: 303.860US1

16/16

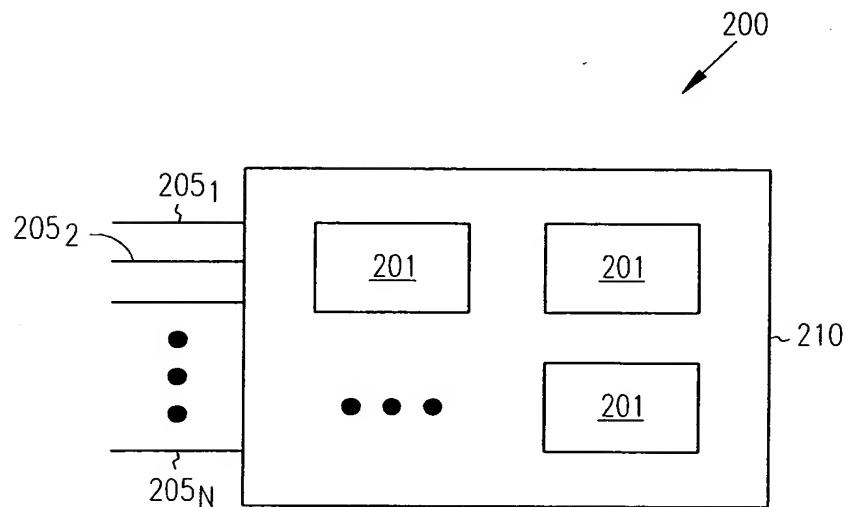


FIG. 26